

OK TO ENTER: /RJ/

**US 10/520,040**

**Examiner: Ryan Johnson, Art Unit 2817**

**Formal Error Correction / Cover Letter (03/02/2009)**

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## **Cover Letter to Commissioner for Patents**

It has been confirmed by phone on Friday that the formal replay mailed to EBC on Monday puts this application into compliance with the interview recommendations and thus it enables passing the application to the legal department for the formal legal verification.

However a correction of obvious formal errors in such approved specification can be accomplished without introducing any new matter, by simple shifting of the entire content of the List of Citations subsection into the proceeding it Background Art subsection.

Such improved integration of the List of Citations accepted already, can also simplify the Background of Invention section by eliminating present separate List of Citations.

Such correction of the formal errors, is shown in the attached documents:

1. Specification Correction - Marked (emailed as `dspmsp-formal_err_cor-spec_marked.pdf`), shows such correction of the Background of the Invention as defined by the marked-up changes of the latest version of this section supplied on the 23 Feb 2009.
2. Specification Correction - Clean (emailed as `dspmsp-formal_err_cor-spec_clean.pdf`), supplies the resulting clean version of such Background of the Invention section corrected with these changes.

The Examiner is earnestly solicited to give favorable consideration to such formal correction and pass it to allowance.

Respectfully submitted,

By: John W. Bogdan on 02 / March / 2009